

SU#09499025

ABSTRACT OF THE DISCLOSURE

A system and method for aligning system clocks is disclosed. A Clock-From-Master (CFM) signal is generated by a circuit allowing arbitrary phase adjustments to a transmit clock signal (tclk) while maintaining a correct relationship between CFM and tclk. Further, the phase of tclk is adjusted to provide an optimal relationship with a receiver clock signal while maintaining the CFM/tclk relationship.

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